

Declaration for Utility or Design Patent Application (37 CFR 1.63)

As a below-named inventor, I hereby declare that my residence, post office address, and citizenship are as stated below next to my name and that I believe that I am the original, first, and sole inventor (if only one name is listed below) or an original, first, and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention, the specification of which is attached hereto and which has the following title:

Methods for Increasing Instruction-Level Parallelism in Microprocessors and Digital Systems

I have reviewed and understand the contents of the above-identified specification, including the claims, as amended by any amendment specifically referred to in the oath or declaration. I acknowledge a duty to disclose information which is material to the examination of this application in accordance with Title 37, Code of Federal Regulations, Section 1.56(a).

I hereby claim the benefit under 35 U.S.C. 119(e) of any United States provisional applications listed below:

Application Number(s)
60/090,782

Filing Date
06/26/1998 (June 26, 1998)

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Title 18, United States Code, Section 1001, and that such willful false statements may jeopardize the validity of the application or any patent issued thereon.

Please send correspondence and make telephone calls to the First Inventor below.

Signature: Sole/First Inventor Derek Wong

Name of Inventor Derek Chi-Lan Wong Date June 25, 1999

Legal Residence San Jose, California, USA Citizen of USA

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San Jose, CA 95120

Telephone (408)927-7940

In the United States Patent and Trademark Office

STATEMENT CLAIMING SMALL ENTITY STATUS (37 CFR 1.9(F) & 1.27(B)) - INDEPENDENT INVENTOR

Applicant: Derek Chi-Lan Wong

Application or Patent No.: _____

Filed or Issued: Filed on June 25, 1999

Title: Methods for Increasing Instruction-Level Parallelism
in Microprocessors and Digital Systems

As a below named inventor, I hereby state that I qualify as an independent inventor as defined in 37 CFR 1.9(C) for purposes of paying reduced fees to the Patent and Trademark Office described in the specification filed herewith with title as listed above, application identified above, or the patent identified above.

I have not assigned, granted, conveyed, or licensed, and am under no obligation under contract or law to assign, grant, convey, or license, any rights in the invention to any person who would not qualify as an independent inventor under 37 CFR 1.9(c) if that person had made the invention, or to any concern which would not qualify as a small business concern under 37 CFR 1.9(d) or a nonprofit organization under 37 CFR 1.9(e).

Each person, concern, or organization to which I have assigned, granted, conveyed, or licensed or am under an obligation under contract or law to assign, grant, convey, or license any rights in the invention is listed below:

1. Derek Chi-Lan Wong
2. Applied Circuits, Computers, and Telecommunications (A.C.C.T.)

Separate statements are required from each named person, concern, or organization having rights to the invention stating their status as small entities. (37 CFR 1.27)

I acknowledge a duty to file, in this application or patent, notification of any change in status resulting in loss of entitlement to small entity status prior to paying, or at the time of paying, the earliest of the issue fee or any maintenance fee due after the date on which status as a small entity is no longer appropriate. (37 CFR 1.28(b)).

Name of Inventor Derek Chi-Lan Wong

Signature of Inventor Derek Wong

Date June 25, 1999

In the United States Patent and Trademark Office

STATEMENT CLAIMING SMALL ENTITY STATUS (37 CFR 1.9(f) & 1.27(c))--SMALL BUSINESS CONCERN

Applicant, Patentee, or Identifier: Derek Chi-Lan Wong

Application or Patent No.: _____

Filed or Issued: Filed on June 25, 1999

Title: Methods for Increasing Instruction-Level Parallelism
in Microprocessors and Digital Systems

I hereby state that I am the owner of the small business concern identified below

NAME OF SMALL BUSINESS CONCERN

Applied Circuits, Computers, and Telecommunications (A.C.C.T.)

ADDRESS OF SMALL BUSINESS CONCERN

1341 Echo Valley Dr., San Jose, CA 95120-5623

I hereby state that the above identified small business concern qualifies as a small business concern as defined in 13 CFR Part 121 for purposes of paying reduced fees to the United States Patent and Trademark Office, in that the number of employees of the concern, including those of its affiliates, does not exceed 500 persons. For purposes of this statement, (1) the number of employees of the business concern is the average over the previous fiscal year of the concern of the persons employed on a full-time, part-time, or temporary basis during each of the pay periods of the fiscal year, and (2) concerns are affiliates of each other when either, directly or indirectly, one concern controls or has the power to control the other, or a third party or parties controls or has the power to control both.

I hereby state that rights under contract or law have been conveyed to and remain with the small business concern identified above with regard to the invention described in: the specification filed herewith with title as listed above, the application identified above, or the patent identified above.

If the rights held by the above identified small business concern are not exclusive, each individual, concern, or organization having rights in the invention must file separate statements as to their status as small entities, and no rights to the invention are held by any person, other than the inventor, who would not qualify as an independent inventor under 37 CFR 1.9(c) if that person made the invention, or by any concern which would not qualify as a small business concern under 37 CFR 1.9(d), or a nonprofit organization under 37 CFR 1.9(e).

Each person, concern, or organization having any rights in the invention is listed below:

1. Derek Chi-Lan Wong

2. Applied Circuits, Computers, and Telecommunications (A.C.C.T.)

Separate statements are required from each named person, concern or organization having rights to the invention stating their status as small entities. (37 CFR 1.27)

I acknowledge the duty to file, in this application or patent, notification of any change in status resulting in loss of entitlement to small entity status prior to paying, or at the time of paying, the earliest of the issue fee or any maintenance fee due after the date on which status as a small entity is no longer appropriate. (37 CFR 1.28(b))

NAME OF PERSON SIGNING Derek Chi-Lan Wong

TITLE OF PERSON IF OTHER THAN OWNER _____

ADDRESS OF PERSON SIGNING 1341 Echo Valley Dr., San Jose, CA 95120-5623

SIGNATURE Derek Wong

DATE June 25, 1999